

[CONVERTING CIRCUIT FOR PREVENTING WRONG ERROR CORRECTION CODES FROM OCCURRING DUE TO AN ERROR CORRECTION RULE DURING DATA READ- ING OPERATION]

Abstract

A converting circuit, for preventing wrong error correction code from occurring due to an error correction rule during data reading operation is provided. When the flash memory controller writes all 0 x FF data into the flash memory, the byte error correction rule generates a set of correct error correction codes and the error correction code converting circuit converts the set of correct error correction codes into 0 x FF error correction codes, and values stored in the data area and error correction code area of the flash memory are converted into 0 x FF to prevent wrong error correction code from occurring during data reading operation when the error correction codes are not completely 0 x FF.